PATENT Docket No.: 02-51786
App. Ser. No.: 10/719,282

IN THE CLAIMS:

Please find below a listing of all pending claims. The statuses of the claims are set forth in parentheses. For those currently amended claims, <u>underlined</u> emphasis indicates insertions and strikethrough emphasis (and/or double brackets) indicates deletions

1. (Currently Amended) A synchronous network establishing method of establishing a synchronous network in which a node apparatus conforming to a first synchronization scheme and a node apparatus conforming to a second synchronization scheme co-reside, wherein the first synchronization scheme and the second synchronization scheme implement different synchronous state indication codes for establishing the synchronous network, said method comprising the step of:

converting a first synchronous state indication code that is supplied from the node apparatus conforming to one of the first scheme and the second scheme into a second synchronization state indication code for the node apparatus conforming to the other one of the first scheme and the second scheme used by the first synchronization scheme into a second synchronous state indication code used by the second synchronization scheme when the node apparatus conforming to the second synchronization scheme receives the first synchronous state indication code from the node apparatus conforming to the first synchronization scheme.

(Currently Amended) The synchronous network establishing method as claimed in claim 1, further comprising-the step-of:

including the first synchronous state indication code that is supplied from the node apparatus conforming to one of the first scheme and the second scheme in an empty bit of the converted second synchronous state indication code.

 (Currently Amended) The synchronous network establishing method as claimed in claim 1, further comprising the step of: PATENT Docket No.: 02-51786

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using a pre-converted synchronous state indication code included in an empty bit of the first synchronous state indication code that is supplied from the node apparatus conforming to one of the first scheme and the second scheme.

4. (Currently Amended) A node apparatus conforming to <u>a given</u> one of a first <u>synchronization</u> scheme and a second <u>synchronization</u> scheme that is connected to a counterpart node apparatus conforming to the other one of the first <u>synchronization</u> scheme and the second <u>synchronization</u> scheme, wherein the first <u>synchronization</u> scheme and the second <u>synchronization</u> scheme implement different synchronous state indication codes for establishing a synchronous network, said node apparatus comprising:

a synchronous state indication code converting unit to convert a for eenverting the-synchronous state indication code <u>used</u> by said other one of the first <u>synchronization scheme</u> and the <u>second synchronization scheme</u> supplied from the counterpart node apparatus into <u>a the other-synchronous state</u> indication code <u>used</u> by said given one of a first <u>synchronization scheme</u> and a second <u>synchronization scheme</u> for said node apparatus conforming to one of the first scheme and the second scheme.

- (Currently Amended) The node apparatus as claimed in claim 4, further comprising:
- a selecting unit to select for selecting one of the synchronous state indication code supplied from the counterpart node <u>apparatus</u> and the converted synchronous state indication code obtained by the synchronous state indication code converting unit.
- (Original) The node apparatus as claimed in claim 5, wherein the selecting unit administers switching according to a switching instruction signal.

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(Currently Amended) The node apparatus as claimed in claim 5, further comprising:

a switch unit to instruct for instructing a switching of the selecting unit.

8. (Currently Amended) The node apparatus as claimed in claim 5, further comprising:

a switching instruction unit to detect for detecting a predetermined-bit of a signal supplied from the counterpart node apparatus to determine which of the first scheme and the second scheme said counterpart node apparatus conforms to, and to instruct instructing-a switching of the selecting unit based on the determination.

(Original) The node apparatus as claimed in claim 4, wherein a content to be converted by the synchronous state indication code converting unit can be arbitrarily changed.